Application Serial No. 10/782,557
Reply to Office Action of August 11, 2005

PATENT
Docket: CU-3590

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:	Tomoo Yamasaki et al.	GRP ART UNIT: 2822
Serial No:	10/782,557	Ex.: Soward, Ida M.
Filed:	February 19, 2004	

For: Capacitor Element, Manufacturing Method Therefor, Semiconductor Device Substrate, and Semiconductor Device

Certification under 37 C.F.R. §1.8(a)

The USPTO Central Fax No. (571) 273-8300
Date of Fax Transmittal: September 1, 2005

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted to the United States Patent and Trademark Office to the fax number and on the date indicated above.

M. Pomroy
Maggie Pomroy

The Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In the Office Action dated August 11, 2005, setting a 1-month shortened statutory period for a reply ending on Monday, September 12, 2005, the pending Claims 1-12 were restricted as follows:

Claims 1-8 and 11-12 drawn to a capacitor element; and

Claims 9-10 drawn to a method of manufacturing a capacitor element.

Applicants hereby elect **Claims 1-8 and 11-12.**

In view of the Examiner's earlier restriction requirement, Applicants retain the right to present the non-elected in a divisional application.

Respectfully submitted,

Dated: September 1, 2005

W. William Park, Reg. No. 55,523
Ladas & Parry
224 South Michigan Avenue
Chicago, Illinois 60604
(312) 427-1300